GOA COLLEGE OF ENGINEERING

FARMAGUDI, PONDA, GOA - 403401 14/11/2018 To 10/12/2018

REVLAUTION EXAMINATION RESULT OF SEMESTER - III (ELECTRONICS & TELECOMMUNICATION) EXAMINATION HELD IN NOV-DEC 2018 (REVISED COURSE)

Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Numbe
Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
3316	Α	М	CHARI UTKARSH ULHAS							1 0200.	, 0.0.00	•		!		•	2017046
1104007																	2017046
BC			Applied Mathematics - III	14		52		66					Р				
			Economics and Management	12		40		52					Р				
			Algorithms for Data Structures	12	14	47		73					Р				
			Electronic Devices and Circuits – I	10		50		60		14			Р				
			Digital System Design	10		46		56		14			P				
			Electrical Circuits and Systems	11		43		54				16	Р				
			Electrical circuits and Systems	11		15	Sem-2 PA		Sem-	-1 PASS		10	•	405		PASSES	
33318 71104009	Α	М	DAIKAR KUNAL GOPALKRISHNA														2017046
Т			Applied Mathematics - III	11		60		71					Р				
			Economics and Management	14		54		68					Р				
			Algorithms for Data Structures	12	11	59		82					Р				
			Electronic Devices and Circuits – I	17		46		63		24			Р				
			Digital System Design	8		50		58		16			Р				
			Electrical Circuits and Systems	13		54		67				10	Р				
							Sem-2 FA		Sem-	-1 PASS				459		R.R.L.E.	
3320	Α	М	DESAI YOGAKSH JAWAHAR														2017050
'1104011 Eneral			Applied Mathematics - III	17		77		94					D				
LIVERAL			Economics and Management			63		73					ı D				
				10	12								P				
			Algorithms for Data Structures	16	12	59		87		47			Ρ				
			Electronic Devices and Circuits – I	7		50		57		17			Р				
			Digital System Design	11		58		69		18			Р				
			Electrical Circuits and Systems	15		68		83	_	4.5466		19	Р				
83331	Α	М	GAWDE BHUSHAN RAGHUVIR				Sem-2 PA	155	Sem	-1 PASS				517		PASSES	
71104023																	2017050
Т			Applied Mathematics - III	15		61		76					Р				
			Economics and Management	12		42		54					Р				
			Algorithms for Data Structures	12	14	54		80					Р				
			Electronic Devices and Circuits – I	18		49		67		20			Р				
			Digital System Design	13		48		61		13			Р				
			Electrical Circuits and Systems	17		65		82				16	Р				
			,				Sem-2 PA		Sem-	-1 PASS				469		PASSES	
33336 1104030	A	М	KAWLEKAR MITANSHU PRAKASH														201705
2			Applied Mathematics - III	10		40		50					Р				
			Economics and Management	10		52		62					Р				
			Algorithms for Data Structures	9	18	62		89					Р				
			Electronic Devices and Circuits – I	7		16		23		15			F				
			Digital System Design	4		31		35		12			F				
			Electrical Circuits and Systems	10		38	\$2	48	\$2	12		17	, D				
			Liectrical Circuits and Systems	10		36	۶۷ Sem-2 F			-1 PASS		17	Г	351	\$2	FAILS	
33338 71104032	A	М	KORGAONKAR BRIJESH BUDHAJI														2017045
ВС			Applied Mathematics - III	10		30		40					F				
			Economics and Management	11		26		37					F				
			Algorithms for Data Structures	5	13	32		50					F				
			Electronic Devices and Circuits – I	10		26		36		21			F				
			Digital System Design	5		17		22		10			F				
			Electrical Circuits and Systems	10		35	\$5	45	\$5	-		19	P				

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Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Number
Roll No	Code	Female		Marks	Marks									Sem-3			
					50/25	Ohta	6	Oletal	6	01-1-4	6				Cunna		
Category	Α	<u>М</u>	KUVELKAR SHANTAN AS VAIBHAV R	50/25		Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
183340 171104034	A	M	RUVELKAR SHANTAN AS VAIBHAV K														201711500
GENERAL			Applied Mathematics - III	10		42		52					Р				
			Economics and Management	10		44		54					Р				
			Algorithms for Data Structures	8	10	45		63					P				
			Electronic Devices and Circuits – I	3		46		49	\$1	19			Р				
			Digital System Design	7		32		39	'	14			F				
			Electrical Circuits and Systems	10		49		59				16	Р				
							Sem-2 PA	SS	Sem-	-1 PASS				365	\$1	FAILS	
183343 171104037	A	F	MAULANKAR DEEPIKA DILIP														201705096
OBC			Applied Mathematics - III	18		50		68					Р				
			Economics and Management	13		27		40					F				
			Algorithms for Data Structures	10	13	57		80					Р				
			Electronic Devices and Circuits – I	11		42		53		22			Р				
			Digital System Design	11		40		51		10			Р				
			Electrical Circuits and Systems	7		54		61				10	Р				
							Sem-2 FA	\IL	Sem	-1 FAIL				395		FAILS	
183357	Α	М	SHET NARVEKAR VARAD GOKULDAS														201704593
171104055																	20170-1030
GENERAL			Applied Mathematics - III	10		51		61					Р				
			Economics and Management	12		45		57					P				
			Algorithms for Data Structures	9	11	58		78					P				
			Electronic Devices and Circuits – I	4		47		51		14			Р				
			Digital System Design	8		40		48	\$2	10			P -				
			Electrical Circuits and Systems	16		42	Sem-2 FA	58	Com	-1 PASS		16	Р	393	42	R.R.L.E.	
183365	Α	F	BARAD DIKSHA DAMODAR				Seili-2 FA	\IL	Seiii	-1 PA33				393	\$ 2	K.K.L.E.	
171104064	^	•	DARAD DIROTA DARIODAR														201705015
GENERAL			Applied Mathematics - III	12		59		71					Р				
			Economics and Management	13		57		70					Р				
			Algorithms for Data Structures	17	17	70		104					Р				
			Electronic Devices and Circuits – I	12		63		75		15			Р				
			Digital System Design	14		40		54		13			Р				
			Electrical Circuits and Systems	17		59		76				19	Р				
			•				Sem-2 PA	SS	Sem-	-1 PASS				497		PASSES	
183368	Α	М	NAIK PRITESH ALIAS VAIBHAV VIKAS														201705004
171104067																	201703004
GENERAL			Applied Mathematics - III	13		60		73					Р				
			Economics and Management	17		79		96					Р				
			Algorithms for Data Structures	18	24	84		126					Р				
			Electronic Devices and Circuits – I	24		81		105		24			P				
			Digital System Design	17		70		87		17			P				
			Electrical Circuits and Systems	19		81	G 2 D4	100		4 0466		20	Р	640		DAGGEG	
183370		м	TILVE SARVAJIT SUDESH				Sem-2 PA	155	Sem-	-1 PASS				648		PASSES	
183370 171104069	Α	М	IILVE SAKVAJII SUDESTI														201704926
GENERAL			Applied Mathematics - III	10		43		53					Þ				
JEHLINAL			Economics and Management	12		62		74					ı D				
			Algorithms for Data Structures	13	20	75		108					ı D				
			Electronic Devices and Circuits – I	7	20	35		42		14			r F				
			Digital System Design	10		41		51		22			r P				
				117													
			Electrical Circuits and Systems	10		50		60				21	P				

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Male Female M	WAINGANKAR NITYANAND VINOD Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN Applied Mathematics - III	Sessional Marks 50/25 13 15 21 8 10 20	Work Marks 50/25	70 44 69 62 40 51	Grace Sem-2 PA	83 59 107 70 50 71	Grace Sem-	21 23	Grace	19	Passes P P P P P P P	Sem-3	Grace	Remarks	PR Number
М	WAINGANKAR NITYANAND VINOD Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	13 15 21 8 10 20	50/25	70 44 69 62 40		83 59 107 70 50 71		21 23	Grace	19	P P P P		Grace		201711489
	Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	13 15 21 8 10 20		70 44 69 62 40		83 59 107 70 50 71		21 23	Grace	19	P P P P	F03	Grace		201711489
	Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	15 21 8 10 20	17	44 69 62 40	Sem-2 PA	59 107 70 50 71	Sem-	23		19	P P P P	F03			201711489
М	Economics and Management Algorithms for Data Structures Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	15 21 8 10 20	17	44 69 62 40	Sem-2 PA	59 107 70 50 71	Sem-	23		19	P P P P	503			
М	Economics and Management Algorithms for Data Structures Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	15 21 8 10 20	17	44 69 62 40	Sem-2 PA	59 107 70 50 71	Sem-	23		19	P P P P	F02			
М	Algorithms for Data Structures Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	21 8 10 20	17	69 62 40	Sem-2 PA	107 70 50 71	Sem-	23		19	P P P	502			
М	Electronic Devices and Circuits – I Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	8 10 20		62 40	Sem-2 PA	70 50 71	Sem-	23		19	P P P	502			
М	Digital System Design Electrical Circuits and Systems GOVEKAR SUBHASHISH KETAN	20		40	Sem-2 PA	50 71	Sem-	23		19	P P	F02			
М	GOVEKAR SUBHASHISH KETAN	20			Sem-2 PA	71	Sem-			19	Р	F02			
М	GOVEKAR SUBHASHISH KETAN	10			Sem-2 PA	SS	Sem-	1 PASS				E02			
М		10										503		PASSES	
	Applied Mathematics - III	10													
		10		35	\$5	45	\$5				Р				
	Economics and Management	12		47		59					Р				
	Algorithms for Data Structures	9	20	48		77					Р				
	Electronic Devices and Circuits – I	9		13		22		21			F				
	Digital System Design	3		18		21		15			F				
	Electrical Circuits and Systems	10		12		22				10	F				
					Sem-2 -	•	Sei	m-1 -				292	\$ 5	FAILS	
F	AKALWADI ASHVINI SATYPPA														201704559
	Applied Mathematics - III	14		28		42					F				
	Economics and Management	12		40		52					+				
	Algorithms for Data Structures	11	24	47		82					+				
	Electronic Devices and Circuits – I	13		40		53		18			+				
	Digital System Design	11		40		51		18			+				
	Electrical Circuits and Systems	10		56		66				16	+				
					Sem-2 -	-	Sei	m-1 -				398		FAILS	
	F	F AKALWADI ASHVINI SATYPPA Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design	F AKALWADI ASHVINI SATYPPA Applied Mathematics - III 14 Economics and Management 12 Algorithms for Data Structures 11 Electronic Devices and Circuits - I 13 Digital System Design 11	F AKALWADI ASHVINI SATYPPA Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design	F AKALWADI ASHVINI SATYPPA Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design	Electrical Circuits and Systems 7 AKALWADI ASHVINI SATYPPA Applied Mathematics - III Economics and Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design Electrical Circuits and Systems 10 12 88 14 28 12 40 40 11 24 47 Electronic Devices and Circuits - I 13 40 Electrical Circuits and Systems	Electrical Circuits and Systems 7 AKALWADI ASHVINI SATYPPA Applied Mathematics - III Applied Management Algorithms for Data Structures Electronic Devices and Circuits - I Digital System Design	Electrical Circuits and Systems 10 12 22 Sem-2 - Sem F AKALWADI ASHVINI SATYPPA Applied Mathematics - III Applied Mathematics - III 14 28 40 52 Algorithms for Data Structures 11 24 47 82 Electronic Devices and Circuits - I 13 40 53 Digital System Design 11 40 51 Electrical Circuits and Systems	Electrical Circuits and Systems 10 12 22 Sem-2 - Sem-1 - F AKALWADI ASHVINI SATYPPA Applied Mathematics - III	Electrical Circuits and Systems 10 12 22 Sem-2 - Sem-1 - F AKALWADI ASHVINI SATYPPA Applied Mathematics - III 14 28 42 Economics and Management 12 40 52 Algorithms for Data Structures 11 24 47 82 Electronic Devices and Circuits - I 13 40 53 18 Digital System Design 11 40 51 18 Electrical Circuits and Systems 10 56 66	10 12 22 20 10	Electrical Circuits and Systems 10 12 22 22 10 F	Electrical Circuits and Systems 10 12 22 22 10 F	Electrical Circuits and Systems 10	Electrical Circuits and Systems 10 12 22 50 F F Sem-1 292 \$5 FAILS F AKALWADI ASHVINI SATYPPA Applied Mathematics - III 14 28 40 52 F F F F F F F F F F F F F F F F F F

Declared on :

Entered by :

Read by :

Checked by :

Principal